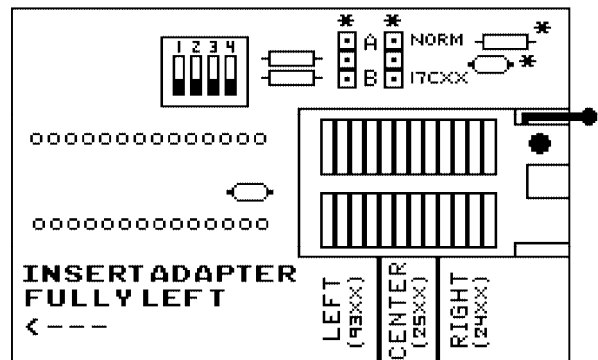


# USING THE SERIAL EEPROM ADAPTER

The serial eeprom adapter (part#ASEREE2) allows the Andromeda Research EPROM+ programming system to read and program all standard 8 pin serial eeproms in a dip package. The adapter uses a 24 pin socket to accommodate the different technology families. The families supported are MicroWire Interface (93 series), SPI bus (25 series) and I2C bus (24 series) plus many others. The adapter will also support the 17CXX family of FPGA configurators from ATMEL with the installation of two optional jumper blocks.



NOTE: The 10K\* resistor and .0022uF\* capacitor on the upper right corner of the adapter board are optional and only required to support the early PCD8572 serial eeprom from Microchip.

## INSTALLING THE ADAPTER

To install the adapter into the programming unit, lift the handle on the programming unit ZIF socket to about 45 degrees. This will release the mechanism and allow the adapter base pins to be inserted into the socket. **NOTE:** This adapter has a 28 pin base and must be fully **left justified** in the 32 pin programming unit socket. After the adapter is inserted and flush with the socket, release the handle to lock the adapter in place. To remove the adapter, reverse the procedure.

## INSERTING EEPROMS INTO THE ADAPTER SOCKET

The ASEREE2 adapter uses a 24 pin socket to accommodate different eeprom technology families. Think of the 24 pin socket as three independent 8 pin sockets; one on the left, one in the center and one on the right. To insert an eeprom into the proper part of the 24 pin socket, left the handle to the vertical position. This will release the socket mechanism. Install the eeprom into proper socket area with the notch or dot on the part facing in the same direction as the red dot on the socket (right). With the eeprom in place, release the handle (horizontal).

The socket is clearly marked as to the location for the different families of standard serial eeproms. Reference the following lists for proper socket location.

**LEFT** - 93XX, 93CXX, 93CSXX, 59CXX, 8011A, 2444, 17CXX\*

**CENTER** - 25XXX, 25CXXX, 95XXX

**RIGHT** - 24XX, 24CXX, 85XX, 85CXX

## DIP SWITCH

The ASEREE2 adapter uses a 4 position dip switch to support certain devices and device features. The setting for the dip switch is displayed when the device with which you are working is selected from the system software or you may recall the setting at any time by using the "S" command.

**IMPORTANT NOTE:** Switch 4 has a unique function as it is connected to a pin used by certain 24 series (I2C bus) devices to write-protect the part. Write protection prevents the internal memory from being altered. If you are experiencing a problem programming a 24 series part, set Switch 4 to on (disable write protect) and again attempt to program the part.

**INSTALLING THE OPTIONAL JUMPER BLOCKS\*** - To install the jumper blocks required to support the 17CXXX FPGA configurators, cut the two short tracks which link the upper two vertical holes at each three position jumper location. **DO NOT** cut the diagonal track which links each set of three holes. Install two .1" three position blocks at each vertical location. Place jumpers at (A)NORMAL for standard operation or (B)17CXXX for programming the configurators.